

NODE INTERCONNECTIONS AND CONTROL

Fig 1

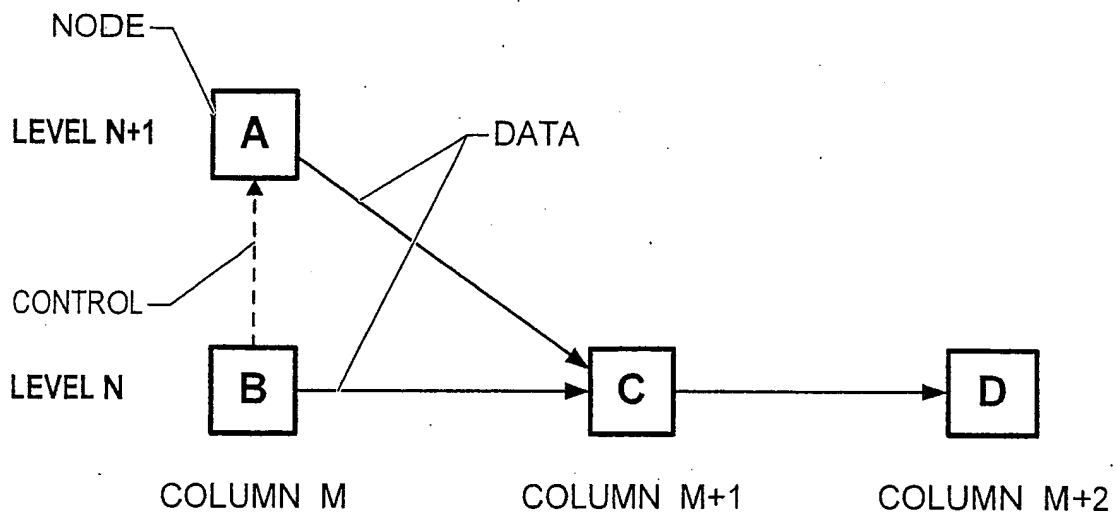


Fig 2

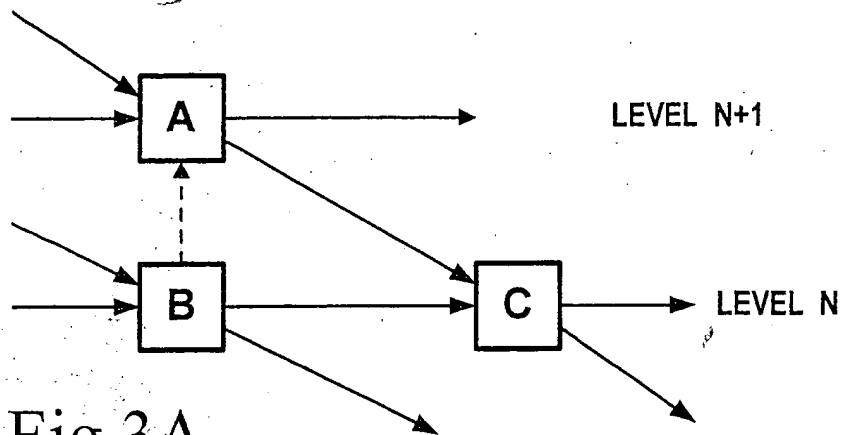


Fig 3A

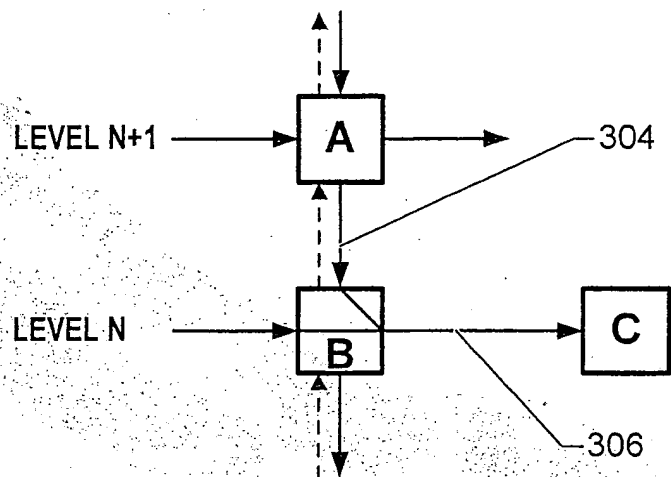
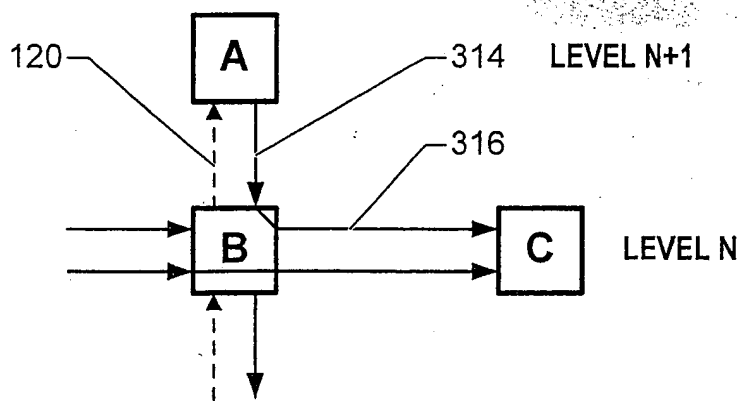


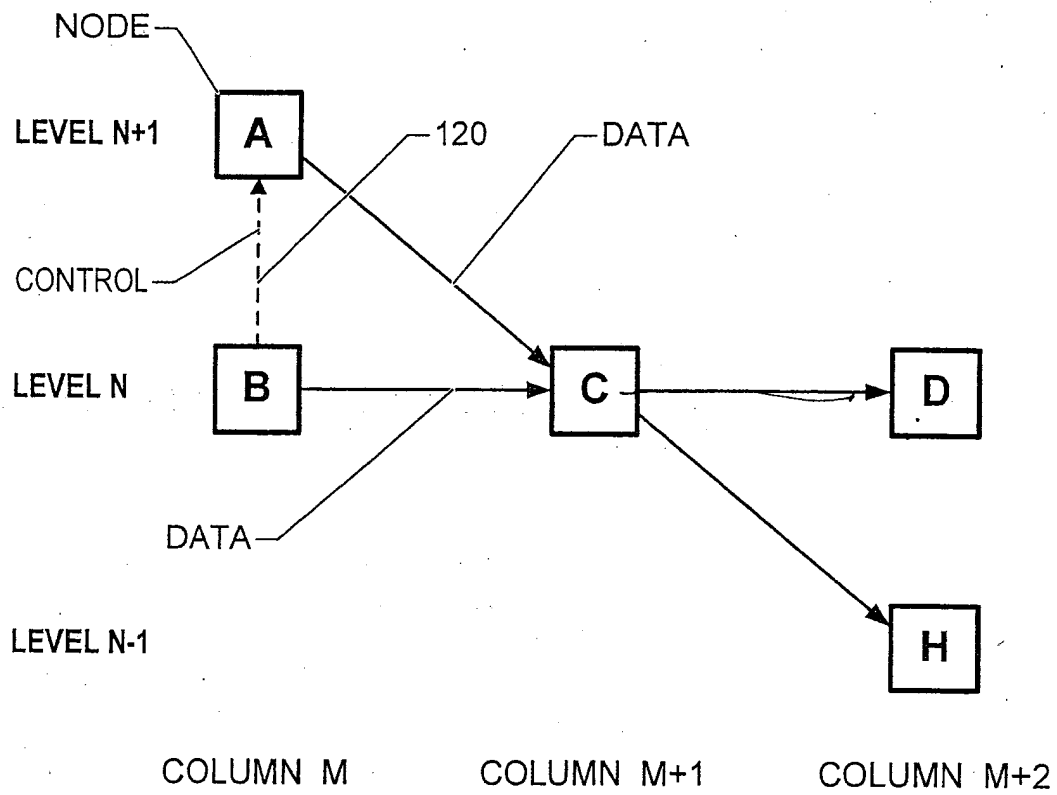
Fig 3B



ALTERNATE NODE INTERCONNECTIONS

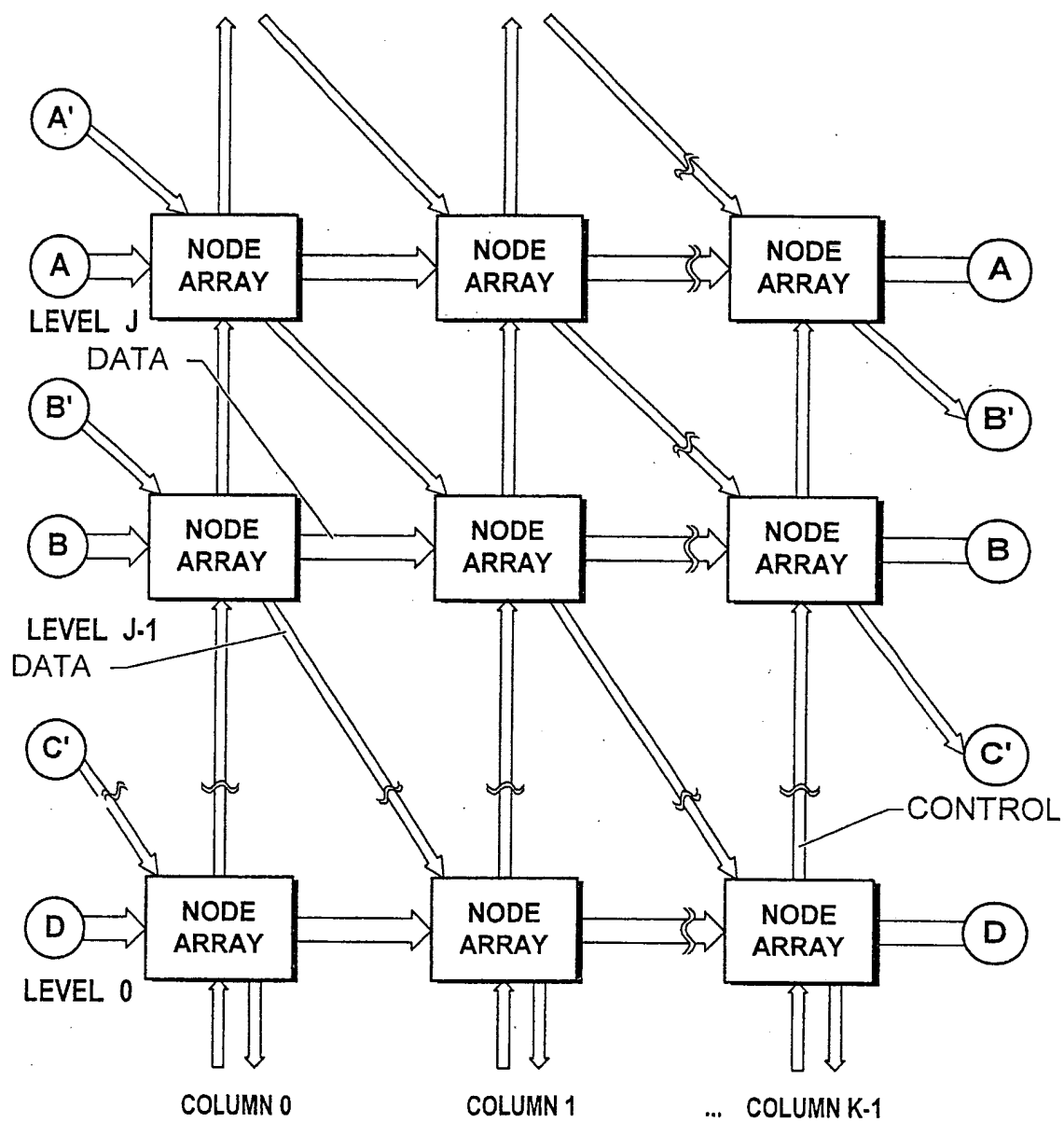
Fig 3C

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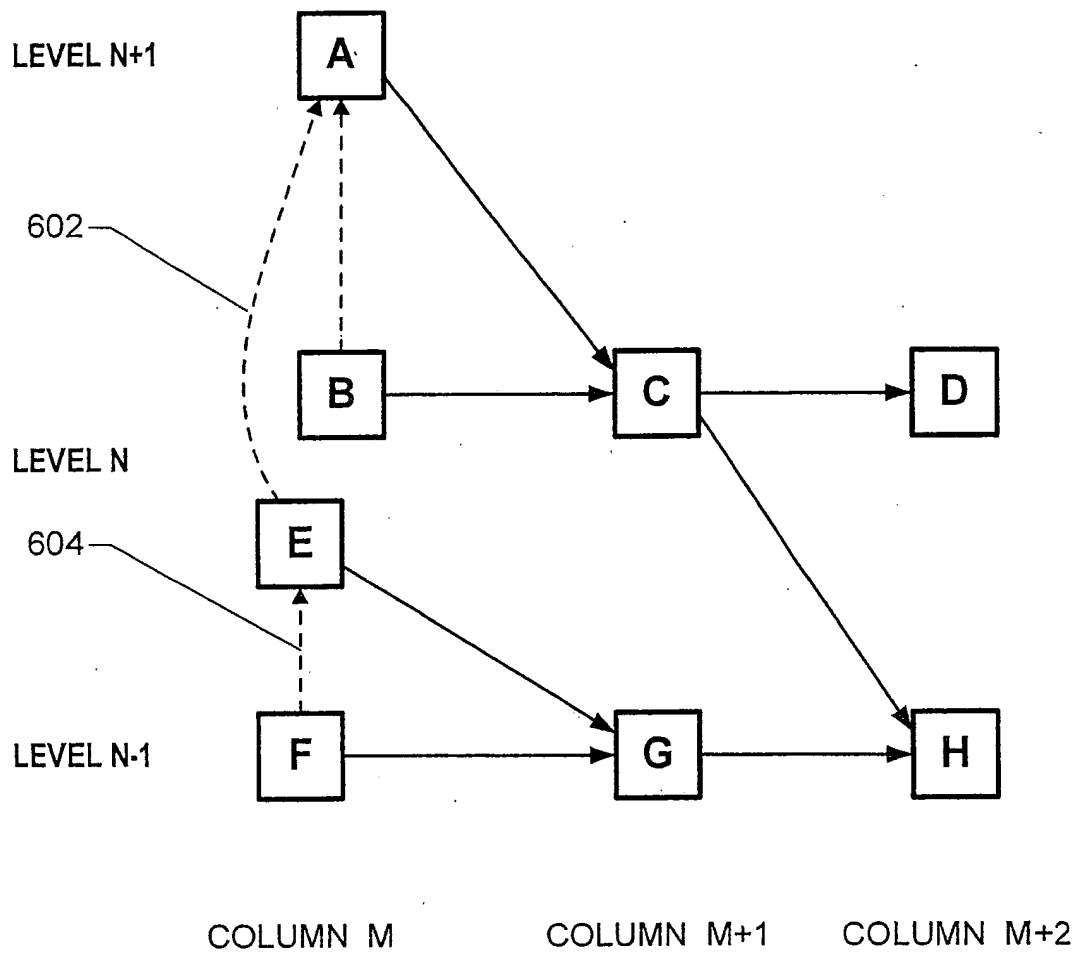
NODES ON THREE LEVELS

Fig 4



INTERCONNECT BLOCK DIAGRAM

Fig 5



INTERLEVEL CONTROL SIGNALS

Fig 6A

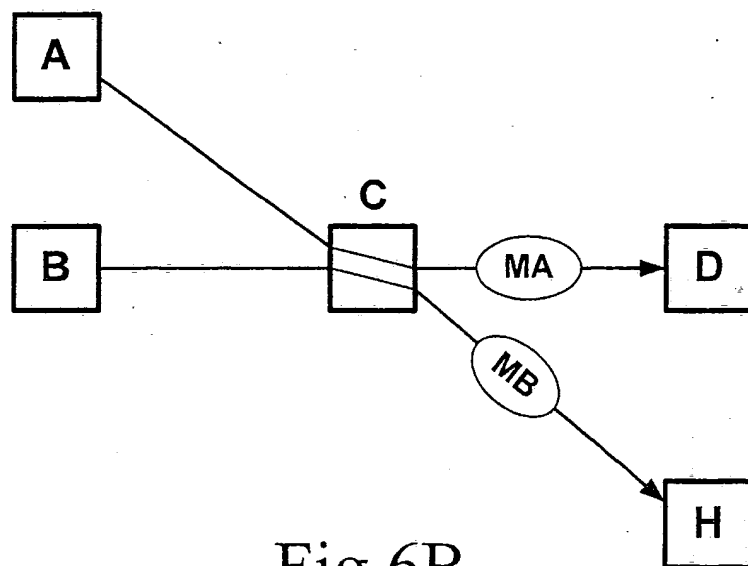


Fig 6B

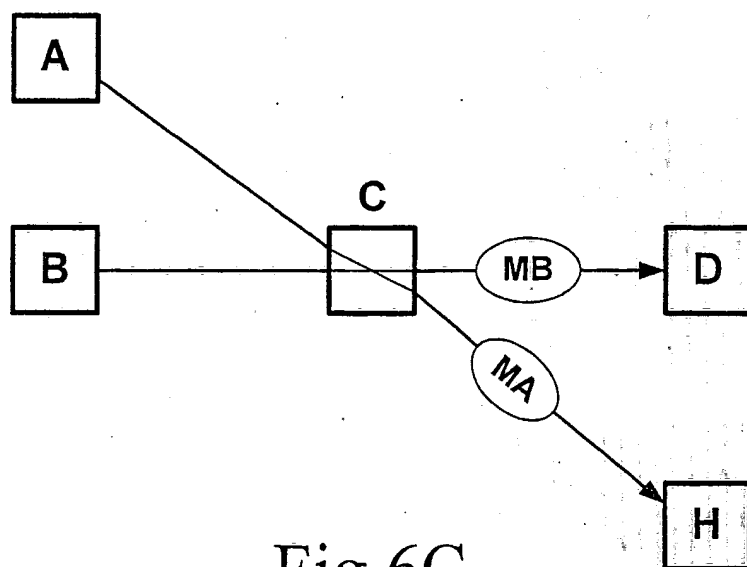


Fig 6C

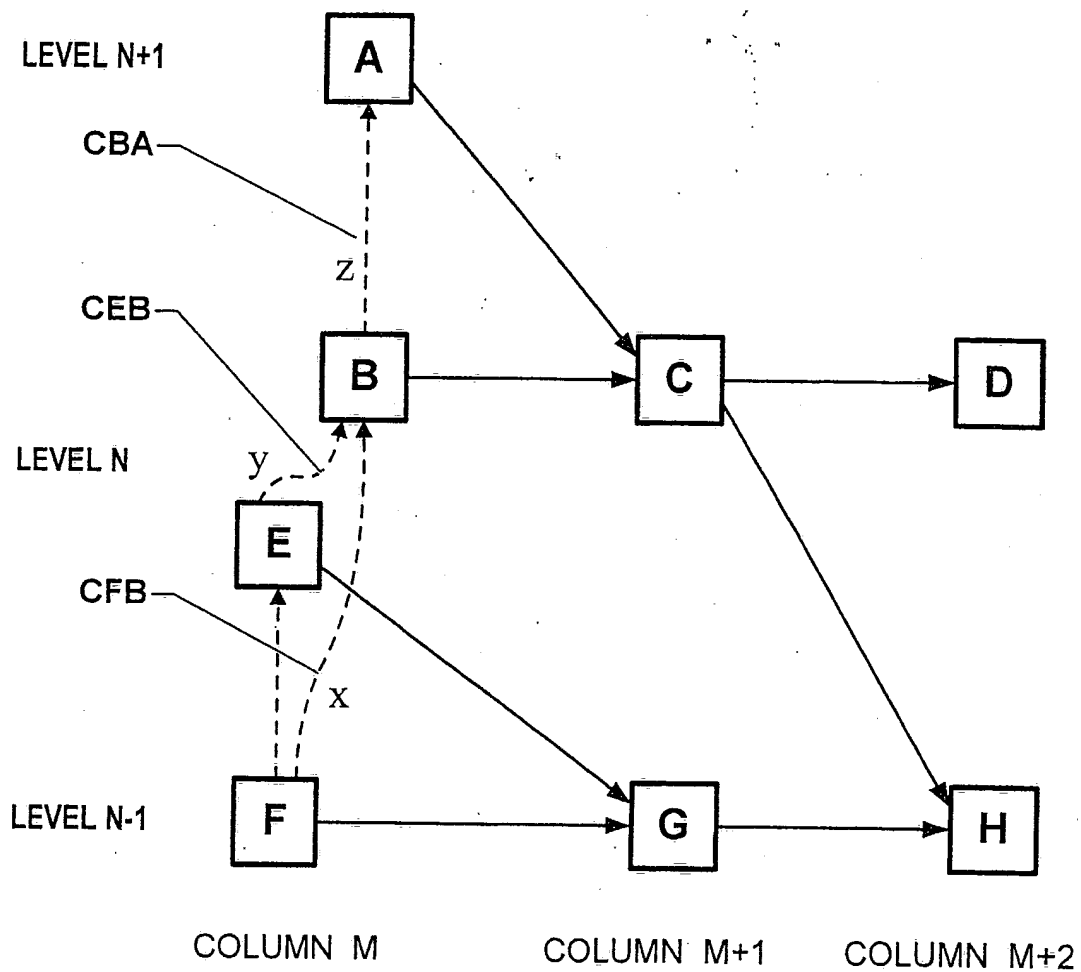


Fig 7

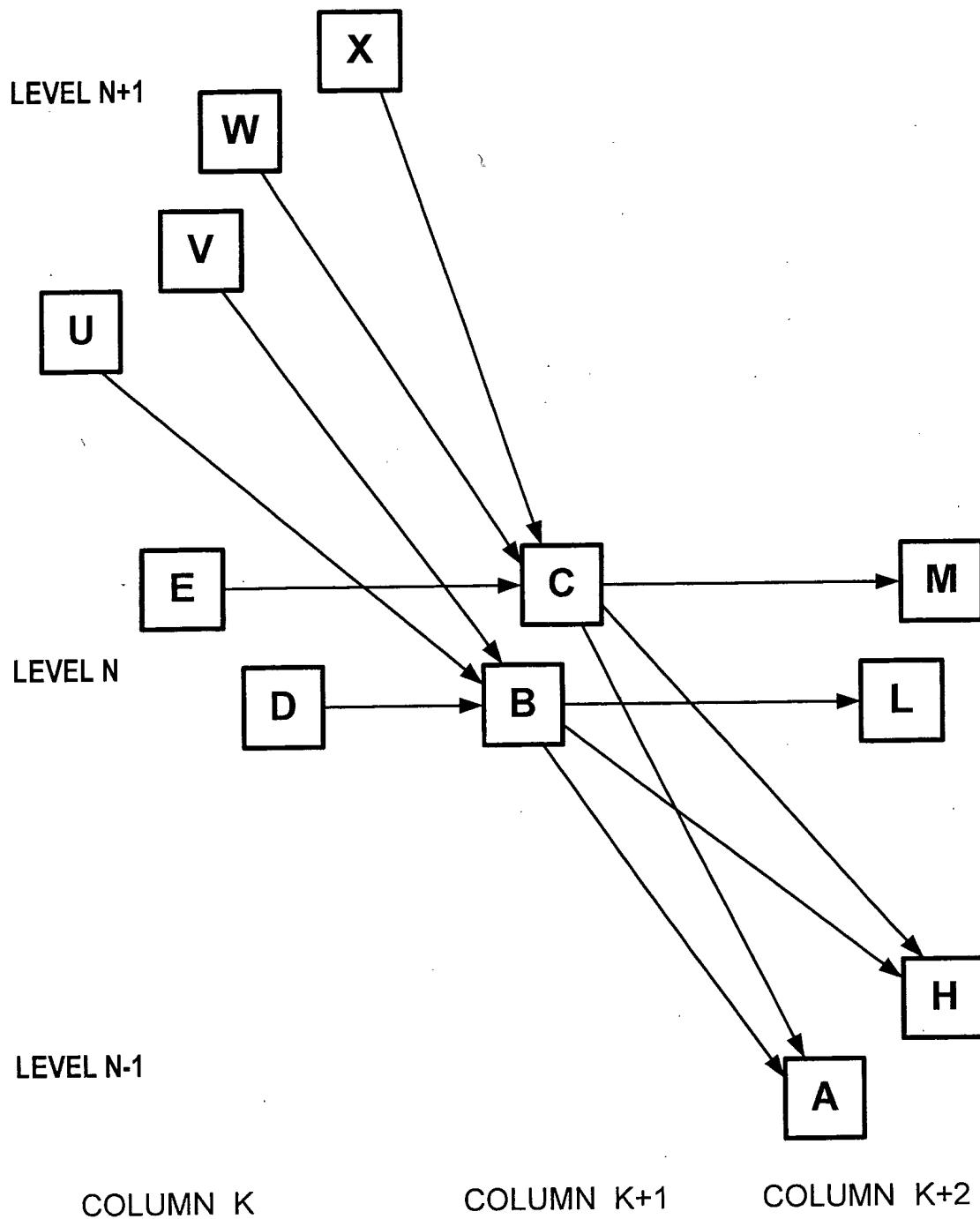


Fig 8